



C. U. SHAH UNIVERSITY, Wadhwan City

Name of Programme: M.Tech

Name of Branch: VLSI & Embedded System Design

Semester: IV

w. e. f. June 2015

| Sr · No | Branch Code | Subject Code | Subject Name | Teaching Schemes (Hours) | | | | Credits | Evaluation Schemes | | | | | | | |
|---------------|----------------|-----------------|--|-----------------------------|----|----|-------|---------|--------------------|-------|-----------------|-------|-------------------|----|-----|-------|
| | | | | Th | Tu | Pr | Total | | Theory | | | | Practical (Marks) | | | Total |
| | | | | | | | | | Sessional Exam | | University Exam | | Pr | TW | Pr | |
| | | | | | | | | | Marks | Hours | Marks | Hours | | | | |
| 1 | 05 | 5TE04DSR1 | Mid Semester Evaluation of Dissertation | - | - | - | - | 09 | - | - | - | - | - | - | 200 | 200 |
| 2 | 05 | | Dissertation | - | - | 30 | 30 | 15 | - | - | - | - | 80 | 20 | 300 | 400 |
| | | | Total | - | - | - | - | 24 | - | - | - | - | 80 | 20 | 500 | 600 |

Th: Theory, To: Tutorial/Seminar/Field work, Pr: Practical, TW: Term Work

Note: Theory _____ 1hr = 1 Credit,

Tutorial/Practical _____ 2hr = 1 Credit,